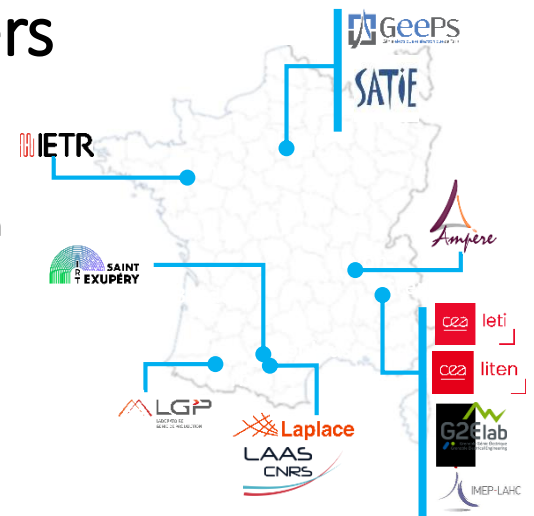
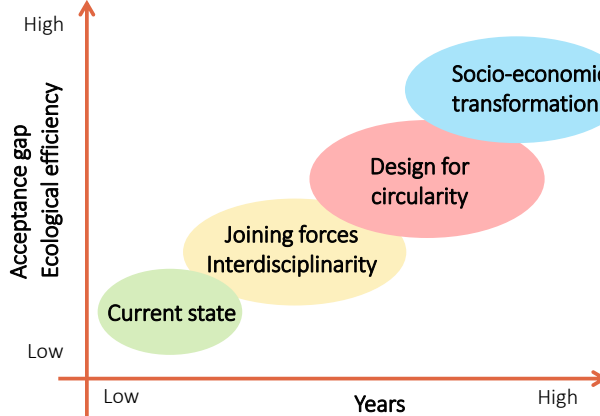


A selection of challenges and barriers of sustainable power electronics

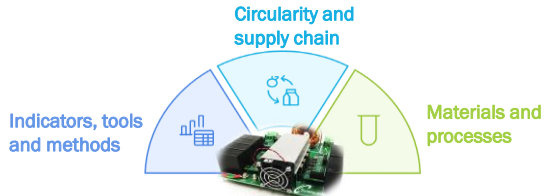
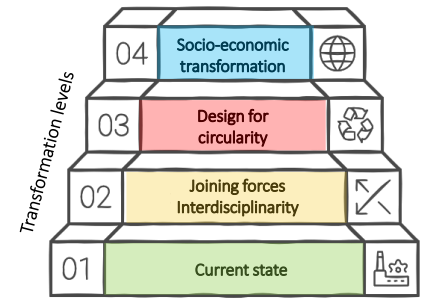


Florentin Salomez ¹, Hugo Helbling ², Morgan Almanza ³, Ulrich Soupremanien ⁴, Guillaume Viné ⁵, Adrien Voldoire ⁶, Bruno Allard ², Hamid Ben-Ahmed ⁷, Daniel Chatroux ⁴, Antoine Cizeron ², Murielle Fayolle-Lecocq ⁹, Suzanne Guillou ⁹, Vincent Grennerat ^{1,10}, Pierre-Oliver Jeannin ¹, Lionel Laudebat ¹¹, Boubakr Rahmani ⁹, Paul-Étienne Vidal ⁵, Luiz Villa ¹², Laurent Dupont ¹³ and Jean-Christophe Crébier ¹.

- 1 G2ELAB, CNRS, Grenoble INP, Université Grenoble Alpes, 38000 Grenoble, France
- 2 Ampère, CNRS UMR5005, INSA Lyon, Ecole Centrale de Lyon, Université Claude Bernard Lyon 1, 69100 Villeurbanne, France
- 3 SATIE, CNRS, ENS Paris-Saclay, Université Paris-Saclay, 91190 Gif-sur-Yvette, France
- 4 Université Grenoble Alpes, CEA-Liten, 17 Avenue des Martyrs, CEDEX 9, 38054 Grenoble, France
- 5 Laboratoire Génie de Production (LGP), Université de Technologie de Tarbes, 65016 Tarbes, France
- 6 Laboratoire de Génie Électrique et Électronique de Paris (GEEPS), CNRS, CentraleSupélec, Université Paris-Saclay, 91192 Gif-sur-Yvette, France
- 7 SATIE, CNRS, ENS Rennes, Université de Rennes, 35170 Bruz, France
- 9 Université Grenoble Alpes, CEA Leti, 38000 Grenoble, France
- 10 IMEP LAHC, CNRS, Grenoble INP, Université Grenoble Alpes, 38000 Grenoble, France
- 11 Laplace, CNRS, Toulouse INPT, UPS, Université de Toulouse, CEDEX 9, 31062 Toulouse, France
- 12 LAAS, CNRS, 7 Avenue du Colonel Roche, 31031 Toulouse, France
- 13 SATIE, CNRS, Université Gustave Eiffel, 78000 Versailles, France



Progression Towards Sustainable Power Electronics

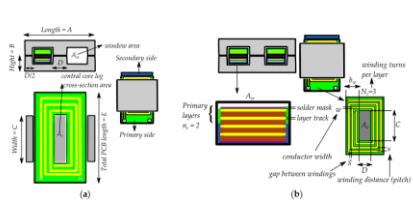
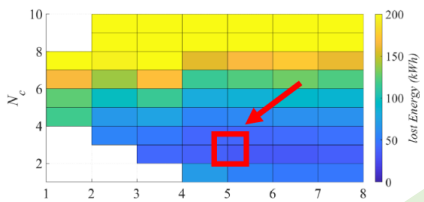
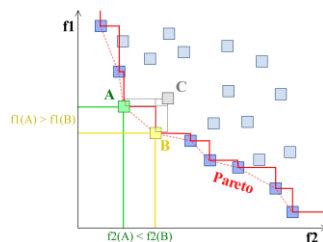


Salomez F, Helbling H, Almanza M, Soupremanien U, Viné G, Voldoire A, Allard B, Ben-Ahmed H, Chatroux D, Cizeron A, et al. "State of the Art of Research towards Sustainable Power Electronics". *Sustainability*. 2024; 16(5):2221. <https://doi.org/10.3390/su16052221>

LEVEL 1 : Current state (Incremental improvement)

Criteria	Level
Efforts	Low
Acceptability (within the field)	High
Transformation factor	Low
Temporality	Now

- ❑ Exposing the gaps in current practices
- ❑ Awareness of traditional limitations
- ❑ Perceived contribution of engineers

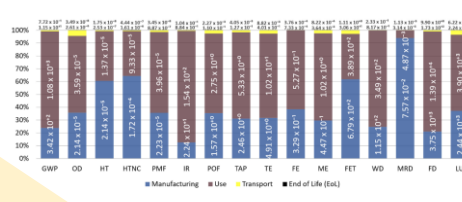


de Freitas Lima, G.; Rahmani, B.; Rio, M.; Lembeye, Y.; Crébier, J.-C. Eco-Dimensioning Approach for Planar Transformer in a Dual Active Bridge (DAB) Application. *Eng* 2021, 2, 544–561. <https://doi.org/10.3390/eng2040035>

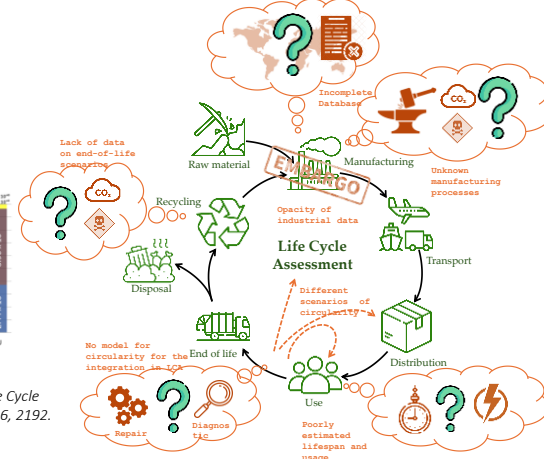
LEVEL 2 : Joining forces (Interdisciplinarity)

Criteria	Level
Efforts	Medium
Acceptability (within the field)	Medium
Transformation factor	Medium
Temporality	ASAP (0-5years)

- ❑ Lack of accurate data from the value chain
- ❑ Opacity of the value chain stakeholders
- ❑ Lack of proper tools and universal methodologies to assess a LCA
- ❑ Lack of a mapping and dynamic classification of PE fields and stakeholders with respect to eco-design and circularity
- ❑ Integrating End Of Life scenarios in LCA



Baudais, B.; Ben Ahmed, H.; Jadin, G.; Degrenne, N.; Lefebvre, S. Life Cycle Assessment of a 150 kW Electronic Power Inverter. *Energies* 2023, 16, 2192. <https://doi.org/10.3390/en16052192>

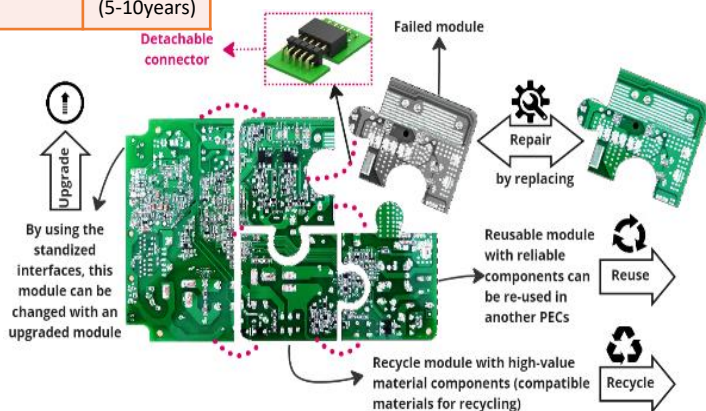


Courtesy : Adrien Voldoire +GT CEPPS

LEVEL 3 : Design for Circularity

Criteria	Level
Efforts	High
Acceptability (within the field)	Low
Transformation factor	Medium
Temporality	ASAP (5-10years)

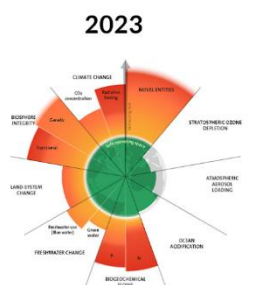
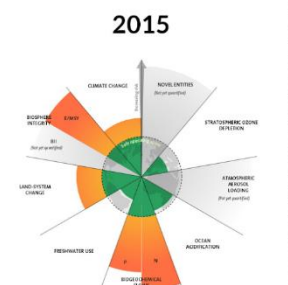
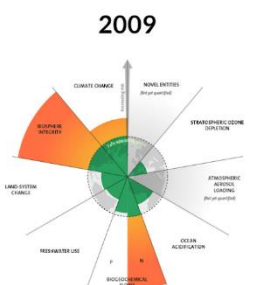
- ❑ Integrate new environmental indicators and design rules into early decision-making in power electronics.
- ❑ Switch design practices and involve new materials, architectures, topologies and design rules
- ❑ Include Economic and Social Sciences to act on the value chain and introduce new products, services, and actors.
- ❑ Rise awareness of the power electronic engineer with respect to the End Of Life of the converter



LEVEL 4 : Socio-Economic transformation

Criteria	Level
Efforts	V. High
Acceptability (within the field)	V. Low
Transformation factor	V. High
Temporality	ASAP (>15years)

- ❑ From better to good enough: Identify the best tradeoff between performance quest and planetary limits
- ❑ From short term competition to long term cooperation: Identify and implement the technical conditions for industrial cooperation
- ❑ From products for consumers, to services for consumers: Identify the evolution of power electronics to sustain device mutualization, massive usage and long lasting products



7 boundaries assessed, 3 crossed

7 boundaries assessed, 4 crossed

9 boundaries assessed, 6 crossed